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Antenna bandwidth engineering through time-varying resistance

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Operational bandwidth of resonant circuits is limited by the resonator's size, which is known as the Chu limit. This limit restricts miniaturization of antennas, as the antenna bandwidth is inversely proportional to its size. Here, we propose slow time modulation of resistive elements to engineer bandwidth of small antennas. The temporal modulation of resistance induces virtual impedance that is fully controlled by the modulation parameters. We show how the virtual impedance can be used to optimize the frequency response of a resonant circuit, leading to enhanced matching at multiple frequencies simultaneously. We experimentally verify the proposed technique, demonstrating enhancement of radiation of a broadband modulated signal radiated by a small antenna.

Performance of small antennas is fundamentally limited by Chu's and Fano's limits¹⁻³. Chu's limit defines a trade-off between the bandwidth and size of passive, linear, and timeinvariant antennas. In order to overcome Chu's limit one needs to utilize either active, nonlinear⁴, or time-varying systems⁵⁻⁹. Currently, there is growing interest towards leveraging time-varying circuit elements to enhance bandwidth of small antennas. It has been shown that matching networks with time-varying reactive elements can break Chu's limit¹⁰⁻¹², while the use of time-varying transmission line parameters allows to overcome the Bode-Fano bound¹³⁻¹⁵. Similarly, temporal modulation of thin absorbers induces spectrum spreading, where the scattered fields can be maintained below a certain level, extending absorber's bandwidth¹⁶⁻¹ Furthermore, direct antenna modulation is also a possibility to overcome bandwidth limitations¹⁹⁻²³. Most of the known approaches exploit time-varying reactive elements, which are prone to instabilities. Other approaches are based on the use of switches, allowing only pulse modulation.

Recently, double-frequency modulation of resistive elements was introduced to enhance bandwidth of small antennas²⁴. In addition, it was shown that for multifrequency input signals, slow modulations of resistive elements can be utilized to achieve perfect impedance matching at multiple frequencies simultaneously²⁵. This method of bandwidth engineer-^{24,25} stems from the equivalence of a time-varying resising tor to a static resistor in series with a virtual impedance element that can be fully reactive. By designing the modulation, the virtual reactive impedance can be controlled at every frequency independently, providing an intriguing opportunity to fully engineer the frequency response. Slowly varying resistive elements have multiple advantages. Firstly, slow modulation is practically more easy to realize than the doublefrequency modulation. Secondly, when the resistance profile is kept always positive, the system is unconditionally stable, in contrast to modulated reactive elements. Finally, as the resistance profile is always positive, there is no power exchange between the main circuit and the modulation circuit.

In this paper, we build on previous works^{24,25} and investigate a possibility of using slowly varying resistive elements to enhance the matching of a narrowband resonant transmitting antenna for all harmonics of multifrequency input signals. We derive expressions for the virtual impedances induced due to the modulation, and apply this technique to enhance radiation from a small antenna at multiple frequencies simultaneously. Finally, we verify the validity of this technique experimentally, where the power radiated by a planar inverted-F antenna (PIFA) is enhanced by leveraging a dynamic resistive element.

It is in principle possible to match a load at different discrete frequencies using lossless time-invariant matching networks, however, there is an important fundamental limitation. The impedances at the considered frequencies must be compatible, and this is not a general feature of input impedances of complex small resonant circuits²⁶. Matching networks for two frequencies are known only for simple basic loads such as single reactive element connected to a single resistor²⁷⁻³⁰ Solutions for matching more than two frequencies become impractical (rather complex and lossy) even if the impedances are compatible at all desired frequencies. In contrast to such multifrequency matching, the technique proposed here has no limit on the number of frequencies that can be matched. In addition, increasing the number of harmonics to form an effectively continuous frequency band should not affect the main results. Still, here we consider discrete harmonics to simplify the analytical and experimental analysis.

Let us consider a voltage source v_s connected to a timevarying resistor as shown in Fig. 1(a). The applied voltage contains two frequency components, and the resistance is modulated at the difference frequency:

$$v_{s}(t) = a_{-1}\cos(\omega_{-1}t + \theta_{-1}) + a_{1}\cos(\omega_{1}t + \theta_{1}),$$

$$r_{t}(t) = r_{0} + r_{0}m\cos(\Delta\omega t + \phi) = r_{s} + r(t).$$
(1)

Here, a_n , ω_n , θ_n , r_0 , m are the voltage amplitude, angular frequency, phase, total static resistance $r_0 = r_s + r_l$, and the modulation amplitude, respectively. The total static resistance is composed by the source resistance r_s and the static component in the varying resistance r_1 . $\Delta \omega = \omega_1 - \omega_{-1}$ is the modulation angular frequency, and ϕ denotes the modulation phase. The modulation amplitude is assumed to be much smaller than unity, and we use the approximation $(1+x)^{-1} \approx 1-x$ in order to calculate the current. Two current components have angular frequencies different from ω_{-1} and ω_1 , and we assume that they are filtered out. In this case, the electric current is

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FIG. 1: (a) Circuit schematic. (b)-(d) Real and imaginary parts of the virtual impedance $Z_{-1,1}^v = R_{-1,1}^v + jX_{-1,1}^v$ as function of the modulation phase ϕ for $\theta_{-1,1} = 0$, where -1, 1 refers to ω_{-1}, ω_1 . Analytical results are marked by [A] and numerical results are marked by [N].

simplified and can be written as

$$i(t) \approx \frac{1}{r_0} \left[a_{-1} \cos(\omega_{-1}t + \theta_{-1}) - \frac{m}{2} a_1 \cos(\omega_{-1}t + \theta_1 - \phi) + a_1 \cos(\omega_{1}t + \theta_1) - \frac{m}{2} a_{-1} \cos(\omega_{1}t + \theta_{-1} + \phi) \right].$$
(2)

Let us use the phasor notation and write the electric current, for example, at only ω_1 :

$$I_{\omega_1} = \frac{a_1}{r_0} e^{j\theta_1} - \frac{m}{2r_0} a_{-1} e^{j(\theta_{-1} + \phi)}.$$
 (3)

On the other hand, the complex voltage amplitude at ω_1 is simply $V_{\omega_1} = a_1 e^{j\theta_1}$. In consequence, the corresponding ratio of the amplitudes of voltage and current reads

$$\frac{V_{\omega_1}}{I_{\omega_1}} = r_0 \left(1 - \frac{m}{2} \frac{a_{-1}}{a_1} e^{j(\theta_{-1} + \phi - \theta_1)} \right)^{-1}.$$
 (4)

Assuming that $m \frac{a_{-1}}{a_1} \ll 1$, we find the equivalent impedance at this frequency as

$$Z^{\rm eq}|_{\omega=\omega_1} \approx r_0 + r_0 \frac{m}{2} \frac{a_{-1}}{a_1} e^{j(\theta_{-1}+\phi-\theta_1)}.$$
 (5)

We see that under this assumption the time-varying resistor is equivalent to r_0 connected in series with a virtual impedance Z^{v} , given by the second term in (5). This equivalent model of the circuit is shown in Fig. 1(a). Depending on the modulation phase ϕ , this virtual impedance can be purely real, purely imaginary, or complex-valued. The same procedure can be done for the other angular frequency, resulting in

$$Z^{\rm eq}|_{\omega=\omega_{-1}} \approx r_0 + r_0 \frac{m}{2} \frac{a_1}{a_{-1}} e^{j(\theta_1 - \phi - \theta_{-1})}.$$
 (6)

By properly selecting the modulation amplitude and phase, we can engineer these virtual impedances. Considering $Z^{v}|_{\omega=\omega_{1}}$, if 1) $\theta_{-1} + \phi - \theta_{1} = h\pi$, we have an extra resistance such that for even values of *h* we add more loss to the circuit and for odd values of *h*, we add gain (negative resistance). On the other

hand, if 2) $\theta_{-1} + \phi - \theta_1 = (2h+1)\pi/2$, the virtual impedance turns out to be purely imaginary, corresponding to a reactance. If *h* is even, this reactance is inductive, and if *h* is odd, the reactance is capacitive.

We confirm the analytical results numerically using the impedance-matrix method³¹. Let us assume that the angular frequencies are $\omega_{-1} = 0.9 \cdot 10^9 \cdot 2\pi$ rad/s and $\omega_1 = 1.1 \cdot 10^9 \cdot 2\pi$ rad/s, which gives rise to $\Delta \omega = 200 \cdot 10^6 \cdot 2\pi$ rad/s. Hence, the modulation frequency is about one fifth of the excitation frequencies. The static resistance $r_0 = 100$ Ohm and $\theta_1 = \theta_{-1} = 0$.

Figures 1(b)-(d) show the real and imaginary parts of the additional virtual impedance $Z_{-1,1}^v = R_{-1,1}^v + jX_{-1,1}^v$ with respect to the modulation phase ϕ , where -1, 1 refer to ω_{-1} and ω_1 . From Fig. 1(b) we see that by tuning ϕ , it is possible to realize pure resistive, pure reactive, or complex-valued virtual impedance. Furthermore, as *m* or a_{-1}/a_1 increases, the real and imaginary parts of the virtual impedance grow. This is due to the fact that the virtual impedance is proportional to these parameters, as is obvious from Eqs. (5) and (6). Thus, harnessing ϕ , *m*, and a_{-1}/a_1 (or a_1/a_{-1}) indeed gives us an opportunity to fully control the virtual impedance.

Concerning the validity of the simplifying assumptions, let us consider two different cases shown in Figs. 1(c) and (d). The first case corresponds to m = 0.25 instead of m = 0.05, while the second case corresponds to $a_{-1}/a_1 = 5$ instead of $a_{-1}/a_1 = 1$. We see that the analytical approximation is rather accurate for m = 0.05, while for m = 0.25 there is a slight disagreement which is more conspicuous in the real part. It is also seen that there is a good agreement between the analytical and numerical results even when $a_{-1}/a_1 = 5$. According to the above observation, we conclude that increasing m plays a more significant role in the accuracy of the analytical results in comparison with increasing the ratio a_{-1}/a_1 .

Next, we leverage the virtual impedance introduced and explained to engineer the bandwidth of resonant circuits. The temporal modulation of a resistive element can be used to create a virtual impedance that compensates the reactive impedance of the resonant circuit at all input frequencies. As a result, the input power at all input harmonics is fully delivered to the load, as the load appears to be matched at all input This is the author's peer reviewed, accepted manuscript. However, the online version of record will be different from this version once it has been copyedited and typeset



FIG. 2: (a) Circuit schematic for voltage source $v_s(t)$ connected to a resonant load with a time-varying resistance r(t). (b) Current harmonics when modulation is off ($m_k = 0$). (c) Current harmonics when modulation is on.

frequencies. Let us consider the amplitude modulated input voltage signal

$$v_{\rm am}(t) = \cos\left(\omega_0 t\right) \left[a_c + \sum_{k=1}^N a_{s_k} \cos\left(\Delta \omega_k t + \theta_{s_k}\right) \right], \qquad (7)$$

where ω_0 is the carrier angular frequency, a_c is a real constant number, N is the number of base-band signal harmonics, a_{s_k} are the amplitudes, $\Delta \omega_k$ are the corresponding angular frequencies, and θ_{s_k} are the corresponding phases. We assume that the bandwidth of the base-band signal $\Delta \omega_N - \Delta \omega_1$ is smaller than $\Delta \omega_1 - \omega_0$. It is useful to express v_{am} as the summation of multiple frequency components as

$$v_{\rm am}(t) = \sum_{n=-N}^{N} a_n \cos{(\omega_n t + \theta_n)}, \qquad (8)$$

where $a_{\pm k} = 0.5 a_{s_k}$, $a_0 = a_c$, $-\theta_{-k} = \theta_k = \theta_{s_k}$, $\theta_0 = 0$, and $\omega_{\pm k} = \omega_0 \pm \Delta \omega_k$. Let us assume that this voltage source is connected to the circuit shown in Fig. 2(a), where the total resistance is modulated in time according to the following rule:

$$r_{t}(t) = r_{0} + \sum_{k=1}^{N} r_{0}m_{k}\cos(\Delta\omega_{k}t + \phi_{k}),$$
 (9)

in which r_0 is the static resistive component that accounts for the static source resistance r_s and the static load resistance r_1 . Also, m_k is the modulation amplitude, and ϕ_k is the modulation phase. Importantly, the modulation frequencies are the same as the base-band signal harmonics. The reactive elements *C* and *L* connected to the circuit resonate at ω_0 , and, as in any resonator, for frequencies ω_k the total reactive impedance is positive (inductive) X_k , and for frequencies ω_{-k} it is negative (capacitive) X_{-k} . For simplicity, we assume that $|X_{-k}| = |X_k|$.

Choosing $\phi_k = \theta_{s_k} + \frac{3\pi}{2}$ induces virtual impedances that can be expressed as

$$Z_{\pm k}^{\mathsf{v}} = j X_{\pm k}^{\mathsf{v}} = \mp j \frac{a_c}{a_{\pm k}} 0.5 m_k r_0.$$
(10)

The chosen modulation phase ϕ_k engineers the virtual impedances so that some virtual positive reactance is induced

at frequencies ω_{+k} , and some virtual negative reactance is induced at frequencies ω_{-k} , or the sign of the induced reactance depends on their position – above or below – with respect to the frequency ω_0 . By tuning m_k and a_c to satisfy $|X_{\pm k}^{\mathsf{v}}| = |X_{\pm k}|$, the circuit becomes purely resistive at all input frequencies, as the virtual reactances $X_{\pm k}^{\mathsf{v}}$ compensate the reactances $X_{\pm k}$ stemming from the dispersive nature of the resonant circuit.

As an example, we consider the voltage source v_{am} having $\omega_0 = 1 \cdot 10^9 \cdot 2\pi$ rad/s, N = 5, and $\theta_{s_k} = 0$. The values of $\Delta f_k = \frac{\Delta \omega_k}{2\pi}$ and a_{s_k} are shown in Table 1, where we assume that a_{s_k} can take values between 1 and 1.5, and the values are chosen arbitrary. The other parameters are as following: $r_0 = 100$ Ohm, $\phi_k = \theta_{s_k} + \frac{3\pi}{2}$, C = 5.07 pF, L = 5 nH, the resonance frequency is ω_0 , and the values of $X_{\pm k}$ are shown in Table 1. To minimize $\sum_{k=1}^{N} m_k$, we choose $a_c = 5$. In the above example, we assumed that $|X_{-k}| = |X_k|$, and in order to perfectly match the circuit we needed to ensure that $|X_{\pm k}| = |X_{\pm k}|$. This is not the case for this example because $|X_{-k}| \neq |X_k|$. Thus, to improve matching of the circuit we can choose m_k that satisfies $|X_{\pm k}^v| = \frac{|X_{-k}| + |X_k|}{2}$. This way we get

$$m_k = \frac{a_k}{a_c} \frac{|X_{-k}| + |X_k|}{r_0}.$$
 (11)

Values for m_k and $X_{\pm k}^{\vee}$ are calculated analytically and are shown in Table 1 along with the numerically calculated total equivalent impedances $Z_{eq}^{eq} = \frac{V_{ab}}{T_{ab}}$.

As we see from Table 1, proper slow temporal modulation of the total resistance reduces the input reactance of the circuit. In this example we manage to keep the magnitude of

TABLE I: Parameters and impedance values

k	Δf_k [MHz]	a_{s_k}	X_{-k}	X_k	m_k	$X_{\pm k}^{v}$	Z_{-k}^{eq}	Z_k^{eq}
1	160	1.1	-11	9.4	0.0224	∓ 10.18	99.7-j0.9	99.5-j0.7
2	170	1.3	-11.7	9.9	0.0282	10.83	99.7-j1.0	99.5-j0.8
3	180	1.5	-12.5	10.5	0.0345	∓11.49	99.7-j1.1	99.5-j0.9
4	190	1.2	-13.3	11	0.0292	12.15	99.7-j1.2	99.5-j1.0
5	200	1.4	-14.1	11.5	0.0359	∓12.82	99.9-j1.3	99.5-j1.1

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FIG. 3: (a)–ADS model of the PIFA antenna where h = 1.5 mm, a = 3 mm, w = 39.32 mm and l = 88.20 mm. (b)–Simulation results for Z-parameters of the PIFA. (c)–Schematic of the whole system where the PIFA antenna is connected to a time-varying resistor. (d)–Profile of the time varying resistance. (e)–Comparison between radiated power P_{rad} by the PIFA antenna when modulation is on/off.

all reactances smaller than 1.3, which is a significant reduction compared to $X_{\pm k}$. The total current $I_t(\omega)$ going through the circuit calculated numerically using the impedance-matrix method³¹ is illustrated in Fig. 2. Figure 2(b) shows the current harmonics when the circuit is static ($m_k = 0$), and Fig. 2(c) shows the current harmonics in the time-modulated circuit. Due to modulation, the reactive components are reduced, and the active components are increased slightly. The modulation also results in some unwanted harmonics, but those that are inside our band of interest (from ω_{-5} to ω_5) have significantly small amplitude, and the unwanted harmonics outside of this band are reactive and can be easily filtered out.

To verify the results experimentally, we study the introduced technique for a resonant load with a time-varying resistor. Specifically, the chosen test load is a typical planar inverted-F antenna (PIFA), and we connect it in series with a time-varying resistor. This way we can experimentally verify the validity of the proposed technique by measuring the power radiated by the PIFA antenna at all input frequencies of a modulated signal. For practical applications, temporal modulation of the source resistance can be used instead of adding an additional time-varying resistive component. Next, we present the antenna topology and the time-varying resistor realization, and show the numerical simulation results.

The prototype antenna is a typical PIFA used in mobile devices. The structure of the antenna is shown in Fig. 3(a) (see Supplementary Material), and Fig. 3(b) shows the input impedance of the antenna between 700 MHz and 1000 MHz.

The realized time-modulated resistor (TMR) is based on a voltage-controlled transistor (MOSFET). The current-voltage (IV) transfer characteristics that determines the TMR performance is the most important factor to choose a proper transistor. To realize a time-varying resistance, an AC control voltage is supplied over a DC gate bias voltage. The control voltage amplitude is chosen so that the transistor operates in the linear regime. The proposed TMR topology is shown in Figs. 3(c). The time-varying resistance operation was first verified in simulations using Advanced Design System (ADS) software. Due to the high-frequency operation regime, the circuit is designed on a PCB with printed transmission lines to guide the signal and supply bias. In addition, a radial stub with a transmission-line section is used as a filter to prevent the high-frequency signal flow to the source supply. Figure 3(d) shows the gate-controlled resistance as a function of time, implemented by a PD20015SE MOSFET transistor. The values for the bias voltages are shown in Table 2. The TMR is nearly sinusoidal and varving with time at the frequency of 30 MHz. as required. The resistance of the transistor when modulation is off is equal to 2.45 Ohm.

Figure 3(c) shows the connection of the PIFA antenna and the TMR circuit in series. The antenna is connected to the signal source V_{sig} in series with the TMR. The antenna terminals are connected between the drain terminal (D) of the MOS-FET and the signal source, therefore, the antenna ground is not connected to the ground of the RF circuit. The source terminal (S) of the MOSFET is connected to the RF ground.

	Value		Power Level	
$V_{\rm DDC}$	1.3 V	Freq.	wo mod.	with mod.
V_{GDC}	4.2 V	850 MHz	-57.86 dBm	-55.33 dBm
V.	200 mV carrier freq. 880 MHz	880 MHz	-31 dBm	-31 dBm
vsig	20 mV AM modulation freq. 30 MHz			
V _{ctrl}	600 mV freq. 30 MHz	910 MHz	-60.68 dBm	-57.10 dBm

DC blocks and chokes are used to protect the source supplies. In this topology, the gate voltage consists of a DC voltage VGDC to open the channel of the transistor and an AC voltage V_{ctrl} to control the varied resistance. For this prototype, the TMR is varied at frequency 30 MHz and operates at a high frequency of 880 MHz. The signal $V_{\rm sig}$ transferred to the antenna includes three harmonics: the carrier harmonic at 880 MHz and two side harmonics at 850 MHz and 910 MHz. The whole time-modulated load consisting of the antenna and TMR is simulated using ADS tools (see Supplementary Material). The values for voltages used in the simulation are shown in Table 2. Here, strong modulation is applied, as seen from Fig. 3(d). The used transistor model (PD20015SE) pre-defines the strength of modulation, as the resistance in each model varies in a specific way. In addition, the voltage amplitudes ratio between the carrier and side harmonics is 10. The equivalent impedance of the transistor can be controlled by varying the modulation phase. For a specific modulation phase, the transistor's equivalent impedance at each side frequency is composed of some reactance and negative resistance. Hence, at this modulation phase the antenna matching is enhanced. Due to the presence of many components in the circuit and transmission lines that add some phase to the propagating signals, accurate calculation of the phase difference is challenging. Thus, the modulation phase is varied until the best performance is achieved.

We simulate the power delivered to the PIFA antenna in two cases: with and without time-varying resistance by turning the control voltage V_{ctrl} on and off. This power is calculated using the current going through and the voltage across the antenna. The power level comparison for these two cases is shown in Fig. 3(e). We observe that the modulation significantly improves the power levels at the two side harmonics at 850/910 MHz. Furthermore, the carrier harmonic at 880 MHz is mostly kept unchanged, again in agreement with the theoretical expectations.

The fabricated antenna is shown in Fig. 4 (see Supplementary Material), while the measured input impedance is shown in Fig. 5(a). The distance between the parallel and series resonances of the tested antenna is larger than the simulated one. This is because in the test antenna, we tune the slot length and deliberately move the parallel resonance further away compared to the simulated model. The measurement setup is shown in Fig. 4. Accurate measurements of the antenna current are difficult at high frequencies. Thus, to validate the effect of the time-modulated resistor on the virtual matching of resonant antenna, a reference horn antenna connected to a spectrum analyzer was used as a receiving antenna to catch the power radiated by the PIFA antenna. Note that, in experiment, the power received by the horn antenna is measured, which is different from the power delivered to the load antenna. Hence, direct comparison between the power levels in simulation and experiment is not relevant. However, the power received by the horn antenna shows enhancement due to modulation similar to the enhancement in the power delivered to the load antenna in simulations. Two signal generators are set up to generate the control voltage to the gate terminal and an amplitude modulated signal to the antenna. The generators are synchronized to allow control of the phase difference between the control voltage, or TMR's phase, and the signal phase. Finally, the transistor of the TMR circuit is biased by a DC voltage to the drain and gate terminals by a DC supply. All values used in the experiment are equal to the ones used in simulations, shown in Table 2.

Figure 5(b) shows the measured power levels at the carrier and two side harmonics that are received by the reference horn antenna. To measure these power levels of the harmonics with



FIG. 4: Measurement setup.





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and without modulation, the modulation signal generated by the controlled signal generator is alternately switched to the "ON/OFF" states. We find that for a certain value of the phase of the modulation signal, the side harmonics are enhanced by approximately 2-3 dB, while the carrier harmonic is kept almost unchanged. The power levels are shown in Table 2. The experimental results confirm the concept of using slow modulation of resistance for impedance engineering.

To realize the desired effect, we manually varied the modulation phase. Although the two generators are synchronized, there is no possibility to measure the actual value of the phase difference between the signal harmonics and the control voltage. Hence, we can only vary this phase shift over the whole 360° span, and then report the best performance, which has been also done in simulations. There are two different mechanisms for compensating the signal distortion due to antenna dispersion: creation of virtual reactances or virtual negative resistances at the side frequencies. As a result of our simulations and experiment we see the expected improvement, but we cannot clearly determine which effect dominates, the virtual reactance or virtual negative impedance. However, in any case, no power is pumped to the system²⁵. This important property is confirmed by the measurements, as the power consumption related to the gate current is negligible. In the context of this work, a negative resistance is a model for the power coupled from other harmonics of the input signal²

In this paper, we have presented a general approach to pass broadband modulated signals through narrowband resonant circuits (for example, small antennas) with minimized distortion and parasitic reflections. The proposed solution is based on time modulations of resistance by an external bias that is controlled by the base-band signal. This can be realized by modulating the source resistance. The proposed solution is protected by a pending patent.

SUPPLEMENTARY MATERIAL

Supplementary Sections 1 and 2 Figure S1 Table S1

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DATA AVAILABILITY

The data that support the findings of this study are available from the corresponding author upon reasonable request.

CONFLICT OF INTEREST

The authors have no conflicts to disclose.

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