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Base Station Antenna Array with Calibration Structure

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Abstract—The introduction of 5G technologies is pushing research in mobile communications to millimeter waves. However, sub-6 GHz frequency range as a part of the 5G standard provides excellent wave propagation properties and more capacity due to new usable frequency ranges. This paper introduces a design for picocell base station 2x2 antenna array with integrated signal injection network for transceiver calibration. Operation frequency range is from 2 to 6 GHz. The antenna array with calibration board has better than -6.3 dB active reflection coefficient and efficiency higher than -3 dB. Calibration network provides 29 dB coupling.

Index Terms—Base station, beamforming, calibration network, mobile communications, sub-6 GHz.

I. INTRODUCTION

According to the Cisco VNI forecast, the compound annual growth rate of mobile data between years 2016 and 2022 is 46 % [1]. This brings challenges to the mobile communication technologies: How to fulfill the ever growing need for higher data throughput? Implementation of 5G standard will offer the solution [2]. With 5G, there has been a discussion on sharing allocated frequency bands which are already used by other applications [3]. In 5G, these Long-Term Evolution (LTE) bands could be combined with WiFi frequency allocations to increase data rates and efficiency of the radio spectrum usage [4].

In addition, sub-6 GHz frequencies propagate better inside buildings than millimeter waves and do not require line-ofsight [5]. One foreseen trend in 5G is network densification: Small picocells are placed to offices and crowd areas outdoors. Therefore it is important that these base stations are imperceptible and small not affecting normal usage of e.g. office space.

5G technologies will utilize electronically steerable antenna arrays. One challenge of phased antenna arrays is potential drifting of phase shifters. Drifting is not typically an issue in reception, because possible drift can easily be calibrated out by adjusting phase shifters to maximize the received power. However, such an easy calibration method can not be utilized in transmission. If the drift is too large, transmitted signal may be directed off from the receiver. To avoid that, a selfcalibration capability can be realized by feeding known signal to all transceiver ports and measuring the difference between the signals [6].

In this paper, we showed the implementation of a picocell antenna array in order to study beam steering in the sub-6 GHz frequency range. The goal is to design an antenna array with a calibration network which could also include at least part of



Figure 1. Vivaldi antenna PCB and balun structure. All dimensions are in mm.

the electronics for the picocell base station and function also as a base structure. The frequency range is from 2 to 6 GHz.

This paper is divided as follows: Section II discusses the details of the design. Introduction of prototype, results from the EM simulations and prototype measurements are presented in Section III. Conclusions from this design are discussed in Section IV.

II. PROPOSED ANTENNA ARRAY DESIGN

The antenna array is designed for a 4-channel transceiver IC. The proposed antenna array design consists of signal distribution printed circuit board (PCB) and two antenna array PCBs. Signal distribution board includes signal paths from the IC to antenna feeds and also the calibration network from calibration signal output to the IC RF-port with equal amplitude and phase. The overall design includes four antennas, four signal traces, and calibration network. These are discussed in more detail in II-A, II-B, and II-C.

A. Antenna array

Vivaldi antenna was chosen for the design due to its ultra wide frequency band and directivity [7]. Both antenna PCBs include two Vivaldi antennas in the middle and an additional parts on the sides as shown in Fig. 1. The additional parts on the sides are used to improve matching below 3 GHz, where the half-wavelength is larger than the opening of the Vivaldi antenna.

Antenna element width (and also element spacing) is a trade-off between the impedance bandwidth and grating-lobe free beam steering range. As a compromise, we chose 50 mm element separation distance. Wider antenna spacing would enable lower cutoff frequency for the antenna and decrease the mutual coupling. However, at higher frequencies the grating



Figure 2. Distribution board with port (P) and antenna (A) locations. Calibration network is highlighted in green and Main RF lines in blue.

lobes would move closer to the main beam and steering capability would be degraded.

The curve of Vivaldi antenna was calculated using equations presented in [8] with the following parameters: $(x_1, y_1) = (0, 0.1), (x_2, y_2) = (50, 25)$ and r = 0.11. The separation between Vivaldi elements is 0.2 mm. Antenna is fed with a sickle shaped 50Ω microstrip line which works as a balun [9].

B. IC location and feeding network

One PCB is used as a base for the array. Upper side of the PCB is dedicated to RF-traces while the other side is reserved for power distribution and logic. RF IC transceiver would locate in the middle of the back side of the PCB to utilize the symmetry of the design. RF signals are routed through vias from one side to the other. In the middle of the PCB, there is a ground plane to isolate these two sides from each other.

The main RF line is identical in all four channels. The line is a 50Ω microstrip that is fed with vias through the PCB to the IC-side and other end is open so that antenna PCBs can be attached to it.

C. Calibration network

The calibration board connections seen in Fig. 2 can be split into three functional sections: Directional coupler, Main RF line, and Calibration network.

Fig. 3a shows the directional coupler used between the calibration network and each Main RF Line. This couples the signal fed from the calibration port to the Main RF Line towards the transceiver IC. Because the desired coupling level is only 25 dB, the coupler is actually designed for much higher frequency to minimize the size of the coupler. Thus, the desired weak coupling is achieved. Other end of the calibration line is terminated to a 100Ω resistor. The directional coupler also introduces insertion loss for the Main RF Line signal path but due to weak coupling, the effect is negligible.

Fig. 3b shows two of the calibration lines, calibration feeding via, and feeding vias for Main RF Lines. Calibration feeding port location has an offset so that RF-IC can be placed in the middle of the board and IC connections can be as close to the via feeds as possible.



Figure 3. Calibration network directional coupler shown in (a) and calibration network in (b). All dimensions are in mm.



Figure 4. Prototype of the proposed design.

III. RESULTS

The combined structure was constructed by soldering two antenna PCBs to the calibration board. Antenna PCBs are mirror images of each other so that the polarity of the antenna remains the same. Fig. 4 shows the prototype of the design. The RF trace side of the calibration board and the balun side of both antenna PCBs are shown in the figure. Five measurement cables are under the structure.

Simulations are done with CST MWS. Measurements are done with two port vector network analyzer (VNA) and by terminating rest of the ports to 50Ω impedance during the



Figure 5. The worst case active reflection coefficient (dB).



Figure 6. The worst case total efficiency (dB).

measurements. 100Ω resistors in the design and calibration kit are Vishay FC0402 [10].

A. Active reflection coefficient

Fig. 5 shows the active reflection coefficient (ARC) in several use cases, when all ports are in the same phase and when the beam is steered. Phase difference of 0 to 210 degrees is studied in 30 degree intervals. The black line shows the envelope curve that represents the worst case ARC over all simulated phase shifts.

The ARC is lower than -6.3 dB from 2.0 to 4.0 GHz and below -9.8 dB from 4.0 to 6.0 GHz. The simulations show that the ARC is heavily dependent on the phase difference between the feed ports and the performance varies when the beam is steered being still at acceptable level.

B. Total efficiency

Total efficiency is simulated with same setup as the ARC. Fig. 6 shows that the total efficiency of the proposed structure is better than $-3 \,\text{dB}$. There is a notable trend of decreasing total efficiency as a function of increasing frequency. This is likely due to FR-4 substrate used in PCB [11]. In addition, the change in the phase of the RF ports affects the total efficiency of the design. Variation between the worst and the best case is the largest at the lowest design frequency.

C. Calibration network performance

The transmission coefficients from the calibration network were simulated and measured from the prototype. The results are shown in Fig. 7. Measured responses correspond well to each other over the whole frequency range. Both the simulated



Figure 7. The measured calibration network performance in transmission coefficients (a) and phase normalized to average phase (b).



Figure 8. Differences of the measured transmission coefficients in amplitude (a) and phase (b) response.

and measured amplitude responses in Fig. 7a have a similar trend up to 3.5 GHz. At higher frequencies, the difference between simulated and measured results increases. S_{15} and S_{45} are 1 dB higher than S_{25} and S_{35} . This is likely result of three additional 90° corners in S_{25} and S_{35} path calibration network traces.

The phase of the transmission coefficient is shown in Fig. 7b. These values are normalized to the average phase of all paths. The relative phase response of all paths over the designed frequency range is steady. S_{15} and S_{25} are very similar. However S_{35} and S_{45} have frequency dependent drift. Physically S_{15} and S_{45} are mirror images of each other. This is also the case with S_{25} and S_{35} . There is a change in slope between mirrored structures. The differences can be explained with small variations of length in the measurement cables soldered to the PCB. At 6 GHz a small difference



Figure 9. 2D cuts of E-plane and H-plane far fields.

in measurement cable length can lead to noticeable phase difference.

The amplitude and phase errors are calculated from Fig. 7 as a difference between the highest and the lowest responses. Fig. 8 analyzes these amplitude and phase errors between simulated and measured prototypes. The amplitude error in Fig. 8a is 1 dB over majority of the design frequency range. The phase error of the measured response in Fig. 8b is twice the simulated values and ranges from 5° to 20° .

Based on these results, the design has a working calibration signal distribution network which can be used to calibrate variance between the RF front ends in the transceiver IC. The possible future research on calibration network could include the fine tuning of the calibration network and replacing soldered cables with connectors on PCB to see how high accuracy can be achieved.

D. Far fields

The far-field patterns are simulated with CST. The results are represented at three different frequencies to show changes in the far-field with ultra-wide frequency range. The far-fields are shown in E-plane and H-plane cuts in Fig. 9. Beam steering capability is limited by the emergence of grating lobes caused by 180° phase shift. Any further beam steering results in a situation where a grating lobe is stronger than the original main lobe. With symmetric far-field, the main lobe is steered 81° at 2 GHz (Fig. 9a), 38° at 4 GHz (Fig. 9b), and 21° at 6 GHz (Fig. 9c) in E-plane. For the H-plane the values are 90° at 2 GHz (Fig. 9d), 39° at 4 GHz (Fig. 9e), and 26° at 6 GHz (Fig. 9f). Therefore the beam steering capability is limited in higher frequencies by the wide relative antenna spacing. We can also see that in H-plane beam direction changes in a linear way as the phase shift is increased. E-plane behavior is not as linear as H-plane. With small phase differences, the beam is steered only a small angle but when the phase shift approaches 150° the beam steering angle changes rapidly.

Fig. 10 shows the far-field antenna properties as a function of main lobe direction. Fig. 10a shows that the realized gain (G_r) of the array is relatively stable over the whole beam steering range in H-plane. The strongest gain is achieved to broadside and it decreases when the beam is steered. The Eplane response is similar except at 4 GHz where the maximum gain is towards 13° .



Figure 10. Realized gain (a), half power beam width (b), and sidelobe level (c) are shown as function of peak beam direction.

The obtained half power beam width (HPBW) of the antenna array correlates with theory. HPBW in the lower part of the frequency band is wide (100°) and becomes more narrow as the frequency increases $(25^{\circ} \text{ at } 6 \text{ GHz})$. There are also differences between E-plane and H-plane HPBW. In H-plane the beam is almost constant when compared to the E-plane. It is obvious that with only two antennas per each steering direction the HPBW remains high in all cases and pencil beam type scanning is not achievable.

The sidelobe level (SLL) of the proposed design is lower than -10 dB at 2 GHz and 4 GHz when no or small phase shift is applied. As the beam is steered away from the normal of the array, the SLL in Fig. 10c decreases until the phase shift between feeding ports is 180° . Then the grating lobe equals the main lobe and thus SLL is 0 dB. Similarly at lower frequencies, when beam is steered to side the grating lobes appear and the SLL approaches 0 dB. This behavior is expected due to fixed antenna element distance and large relative difference between the frequency band limits.

IV. CONCLUSION

A picocell base station antenna array was designed and investigated to harness the potential of the sub-6 GHz frequency range. A calibration network for IC-transceiver and four element Vivaldi array were designed and successfully combined together. Based on the simulations and measurements, the proposed design provides beam steering capability with ultrawide frequency band. Internal calibration network can be used for transceiver IC calibration.

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