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T. Elo,1,a) T. S. Abhilash, M. R. Perelshtein,1,2 I. Lilja,1 E. V. Korostylev,2 and P. J. Hakonen1,a) ✉

AFFILIATIONS
1Low Temperature Laboratory, Department of Applied Physics, Aalto University School of Science, P.O. Box 15100, FI-00076 Aalto, Finland
2Moscow Institute of Physics and Technology, Moscow 141700, Russian Federation

Authors to whom correspondence should be addressed: teemu.elo@aalto.fi and pertti.hakonen@aalto.fi

ABSTRACT

We present a lumped-element Josephson parametric amplifier (JPA) fabricated using a straightforward e-beam lithography process. Our strongly coupled flux-pumped JPA achieves a gain of 20 dB with a bandwidth of 95 MHz around 5 GHz, while the center frequency is tunable by more than 1 GHz, with the additional possibility for rapid tuning by varying the pump frequency alone. Analytical calculations based on the input-output theory reproduce our measurement results closely.

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Low-noise amplification of microwave signals is a key requirement in numerous experiments in quantum technology, including qubit readout, optomechanics, and quantum sensors. Since current state-of-the-art semiconductor amplifiers add 1–3 K of noise to the measured signal,1 Josephson parametric amplifiers (JPAs),2,3 along with other superconducting amplifiers,4–7 having added noise close to one quantum have gained significant interest. The active component in a JPA is the nonlinear inductance of a Josephson junction (JJ), modulating the resonance frequency of a resonator, in first realizations formed by a transmission line cavity.8–11 However, the bandwidth of cavity-based JPAs is limited to a few megahertz due to the high quality (Q) factor of the cavity.

To achieve higher bandwidths, a lumped-element JPA was introduced,12 where the JJ and a capacitor form a parallel LC resonator, maximizing the ratio of Josephson inductance to the total inductance. The behavior of a lumped-element JPA can be tuned over a wide range by changing the coupling strength. Besides the high coupled Q configuration,13 one can omit the coupling capacitor resulting in a low coupled Q and wide bandwidth,14 of up to 100 MHz, which can be increased further by engineering the impedance of the environment.15,16 However, high-performance JPAs are relatively complicated to fabricate, requiring deposition of low-loss dielectrics for parallel plate capacitors and low-impedance vias14 or optical lithography for niobium followed by e-beam lithography for two aluminum layers.13

We present a lumped-element JPA utilizing a straightforward fabrication process. The JJs, capacitor, flux line, and bonding pads are defined in a single e-beam lithography step followed by double-angle evaporation of aluminum. We employ a bridgeless shadow evaporation technique,17,18 allowing us to fabricate JJs with high critical currents by increasing the surface area instead of lowering the oxide thickness, which could lead to increased variance of critical currents.

First, we consider the theoretical framework of our lumped-element JPA, operating at frequency ωr consisting of a nonlinear resonator formed by a shunt capacitor and a SQUID which is pumped with external RF magnetic flux through the SQUID at frequency ωp ≈ 2ωi (three-wave mixing). We chose this operation regime, because for a four-wave mixing (typically with a current pump with ωp ≈ ωi), the large amplitude pump is within the amplification bandwidth, whereas the three-wave mixing process separates the pump tone from the amplified signals, therefore simplifying the practical use of the JPA. We can write down the Hamiltonian of a system under study in the rotating wave approximation19

\[
\hat{H} = \hbar \xi_r \hat{a} \hat{a}^{\dagger} - \frac{\hbar}{2} \left[ \omega_r \hat{a} \hat{a}^{\dagger} + \kappa_{\text{eff}} \right] .
\]

(1)

In this formula, \(\xi_r = \omega_p \gamma - \omega_r\), where \(\omega_r\) is the resonator frequency, \(\gamma\) is the strength of the pump, and \(\kappa_{\text{eff}}\) is the cavity mode. Solving the Quantum Langevin Equation (QLE) with the Hamiltonian above yields the signal gain as a function of frequency. In QLE, we take into consideration a cavity decay rate \(\kappa\), which is determined by the JPA quality factor \(Q\): \(\kappa = \omega_0/Q\). Since our JPA is strongly coupled to the environment, its quality factor is dominated by coupling, and
therefore, $Q \approx Q_c \left( \frac{Z_0}{\sqrt{C/L}} \right)$. The resulting amplification of the signal at frequency $\omega$ as a function of detuning $\xi = \omega_p/2 - \omega$ is given by

$$G(\xi) = \left| 1 - \frac{\kappa \chi(\omega_p/2 + \xi)}{1 - |x|^2 \chi(\omega_p/2 + \xi) / \chi(\omega_p/2 - \xi)} \right|^2,$$

where $\chi(\omega) = (\kappa/2 - i(\omega - \omega_p))^{-1}$ is the electrical susceptibility of the resonator. It can be seen from the equation that gain is achieved at various resonator detunings, but to clarify this dependence, we fix the pump power to $\sigma_{\text{max}} = \kappa/2$, corresponding to infinite gain at zero detuning. Now, by substituting $\chi = (\kappa/2 - i\kappa)^{-1}$, where we introduce $\sigma = \xi/\kappa$, the maximum gain at a given resonator detuning is given by

$$G(\sigma) = \left| 1 - \frac{\sigma}{1 + \sigma^2} \right|^2.$$

In the high-gain limit, we can assume that $\xi \ll \kappa$, and in terms of quality factor, this relation can be written as $G = \left| 2Q_{\text{max}}/\omega \right|^2$. Consequently, we can express the pump frequency range where the gain exceeds a given value $G_0$

$$\omega_p(G_0) = \omega_0 \left( 2 \pm \frac{1}{\sqrt{G_0 Q_{\text{max}}}} \right).$$

Then, in addition to the wide bandwidth, JPAs with a low quality factor has the advantage of wide tunability by varying the pump frequency. This tuning via $\omega_p$ is a considerably faster operation than changing the DC flux value. This rapid tuning would be beneficial in various frequency-multiplexing schemes, e.g., in sequential readout of resonators at different frequencies. If the JPA frequency switching time is sufficiently short, multiple resonators can be measured before the ringdown of the resonators. In general, when the pump power is not fixed, it is possible to use $\eta = 2/\sigma_{\text{max}} = 2 \kappa/\xi$ to evaluate the final maximum gain function of the two parameters controlled in the experiment

$$G(\sigma, \eta) = \left| \frac{(\eta^2 + 4\sigma^2 + 1)^2}{\eta^2(8\sigma^2 - 2) + (4\sigma^2 + 1)^2} \right|^2,$$

where $\eta \leq 1$. Here, if $\eta = 1$, this equation turns into Eq. (3). Note that in the case $\sigma = 0$, meaning zero resonator detuning, gain tends to infinity.

The design of our JPA is shown in Fig. 1. The layout design follows a lumped-element principle: no transmission lines are required since all distances are kept short compared to the wavelength ($\lambda \approx 30 \text{ mm at 10 GHz}$). Instead of a ground plane spreading over the entire chip, we place the SQUID and the capacitor in proximity to two bonding pads. The JPA is tuned and pumped with an external AC + DC flux through the SQUID loop, applied with an excitation line whose geometry is designed to reduce parasitic coupling to the loop formed by the SQUID and the capacitor.

While the resonance frequency of the JPA is given by $\omega_p = 1/\sqrt{LC}$, the amplification bandwidth is inversely proportional to quality factor $Q = Z_0 \sqrt{C/L}$ and thus related to critical current of the SQUID as $1/I_c$. On the other hand, the saturation power of the JPA is directly proportional to $I_c$, resulting in a trade-off between the bandwidth and saturation power. Moreover, JPAs with low $Q$ require strong pumping, which may cause unwanted nonlinear behavior. However, in our application, a wide bandwidth is preferred over high saturation power, and therefore, we set $Q \approx 2$, resulting in $C = 1.2 \text{ pF}$ and $L = 0.8 \text{ nH}$, obtained by applying a DC magnetic flux of $\Phi_{\text{DC}} \approx 0.4IB_0$ through a SQUID with a total unbiased of $I_c = 1.2 \mu\text{A}$.

The capacitor was realized as an interdigital capacitor due to its low losses and good suitability to our e-beam lithography process. The capacitor is oriented so that the rotational axis of evaporation is perpendicular to the capacitor fingers, allowing proper control over the finger widths. The 1.2 pF capacitor has 62 fingers with a length of 330 $\mu\text{m}$ and a width and a gap of 2.4 $\mu\text{m}$, connected to the bonding pads directly, thus minimizing parasitic inductance. Since large interdigital capacitors may exhibit nonideal behavior at high frequencies, we verified our capacitor design with electromagnetic (EM) simulations. In addition, EM simulations were used to estimate the total geometric inductance of the JPA device, resulting in less than 10% of the total inductance in the operating range.

For the Josephson junctions, we chose a small critical current density (7 A/cm$^2$) to improve the quality and reproducibility of the JJs. This, combined with the 600 nA critical current of the JJs, results in a junction area of $\sim 9 \mu\text{m}^2$. Since such JJs are difficult to fabricate using the Dolan bridge technique, we employed a bridgeless shadow evaporation process, where the layer separation is realized in the leads connecting the JJs: the lines leading up- and downwards from the JJ in Fig. 1 are formed by the first and second aluminum layers, respectively. We have also fabricated similar JPAs using the bridge technique using higher critical current density.

The JPAs were fabricated on an oxidized silicon substrate by e-beam lithography using double layer resist [Copolymer methacrylate - methacrylic acid (MMA-MAA) 700 nm + PMMA 200 nm] and a 100 kV beam to achieve low parasitic undercut although 20 kV is also feasible if e-beam doses are well-adjusted. The process involved a double-angle ($\pm 45^\circ$) evaporation of 75 nm thick aluminum, separated by in-situ oxidation at 15 mbar for 10 min.
Our measurement setup is illustrated in Fig. 2. The measurements were conducted at 30 mK using a BlueFors LD-250 dry dilution cryostat. The JPA was protected from external magnetic fields with a cylindrical Cryoperm shield and a superconducting (Pb) inner shield. The DC flux bias and the RF pump shared a common on-chip flux line, and the signals were combined by an external bias-tee.

First, we characterized the tunability of the JPA resonance frequency as a function of DC magnetic flux applied through the SQUID loop. Fig. 3 shows the phase of the reflected signal and the calculated estimates for resonance frequency using both the ideal and EM-simulated capacitor model including parasitic inductance. Although our capacitor contains parasitics, we observe that at the desired working point at 5 GHz, the deviation between the two capacitor models is negligible, meaning that the capacitor can be considered nearly ideal at those frequencies.

We then characterized the JPA gain at various values of $\Phi_{DC}$. Our JPA exhibited $>13$ dB gain at $0.3 \Phi_0 < \Phi_{DC} < 0.4 \Phi_0$, and the widest tunability was obtained at $\Phi_{DC} = 0.36 \Phi_0$. The maximum gain at that point is plotted in Fig. 4 as a function of pump frequency and pump power entering the JPA, omitting reflection and finite coupling of the pump tone to the SQUID. The observed variation of the pump power at maximum gain was most likely caused by minor resonances in the flux pumping line, causing the actual RF flux amplitude through the SQUID to fluctuate as a function of the pump frequency at constant applied pump power. It should be noted that the operation frequency can be tuned by several hundred megahertz varying the pump frequency alone, as shown in Eq. (4), while additional tunability from 4.8 to 5.8 GHz can be achieved by varying the DC flux as well.

Noise performance of the JPA was characterized using the signal-to-noise ratio (SNR) improvement method, giving noise relative to system noise temperature, which was calibrated separately using the Y-factor method with a heated load. Thus, we can estimate the noise temperature of the JPA using the following relation:

$$T_{JPA}(\omega) = T_{tot}(\omega) \left( \frac{1}{\eta_{SNR}(\omega)} - \frac{1}{G(\omega)} \right),$$

(6)

where $T_{tot}(\omega)$ is the total system noise referred to the JPA input port including the noise of the HEMT preamplifier and losses in cables and circulators, $\eta_{SNR}(\omega)$ is the SNR improvement, and $G(\omega)$ is the gain as defined in Eq. (2). Our system noise has a mean value of about 6 K.

The gain and noise characteristics of the JPA in the denoted operating points are shown in Fig. 5. The dashed red line marks the calculated gain outline, showing good agreement with the experiment. In calculations, we used $\Phi_{DC} = 0.36 \Phi_0$ for DC flux and $\Phi_{RF} = 0.07 \Phi_0$.
for RF flux, and the strength of the pump was 0.45 \( k (=0.9 \times k_{\text{max}}) \). The observed 20 dB gain with a bandwidth of 95 MHz is of the same order as reported previously for a strongly coupled lumped-element JPA.\(^7\) The noise performance approaches the quantum limit (\( T_Q = \hbar \kappa_b / k_b \)) within the measurement error, as expected for a low-loss JPA. We attribute the large error to the inaccuracies in cable losses and the system noise temperature measurement. Since our design was optimized for the wide bandwidth, the determined input power resulting in 1 dB gain compression was \(-125 \pm 3\) dBm, which, although low, is adequate for various experiments. One example is measurements of low-bias shot noise, with noise powers equivalent to a few millidegrees Kelvin, which currently require long integration times with HEMT amplifiers.\(^8\) Additionally, the saturation power is sufficient for superconducting qubit readout, where a typical photon occupation in a readout resonator \( n = 3 \) with effective \( Q = 1000 \) corresponds to a probe power of \( P \approx -130\) dBm.\(^9\)\(^,\)\(^10\)

In summary, we have designed and fabricated a JPA using a straightforward fabrication process with single-step e-beam lithography. The design favoring a wide bandwidth over a high dynamic range resulted in nearly quantum-limited noise performance with a gain of 20 dB and a bandwidth of 95 MHz, which will be useful, e.g., in shot noise spectroscopy, qubit readout, and quantum vacuum measurements,\(^9\)\(^,\)\(^10\) where signal levels are low. Varying the pump frequency allows rapid tuning of the JPA band center by several hundred megahertz. Because the JPA is relatively simple to fabricate, the design can be easily modified to meet the requirements of various experimental settings. These modifications include, for example, having additional SQUID loops in series to enhance the saturation power\(^9\)\(^,\)\(^10\) or placing an impedance-matching waveguide structure either on- or off-chip to improve the amplification bandwidth.\(^9\)\(^,\)\(^10\)

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