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Estimation of an Unbalanced Grid Impedance Using a Three-Phase Power Converter

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Abstract
This paper proposes a real-time method for estimating an unbalanced grid impedance using a three-phase converter. In the method, a periodic single-frequency or multi-frequency excitation signal is added to the converter voltage reference. The converter measures currents and voltages at the point of common coupling. The impedance estimate is obtained from the measurements using sliding discrete Fourier transform (SDFT). The method is experimentally validated.

1. Introduction
Grid converters provide an interface between renewable energy sources and the grid. For control of these converters, the grid is often modeled as a three-phase voltage source and a balanced series impedance. The balanced impedance $Z_g$ can be expressed with a transfer function such as $Z_g(s) = sL_g + R_g$, where $L_g$ is the inductance and $R_g$ is the resistance of the grid. Several methods for estimating the balanced grid impedance using a converter have been proposed, see [1–10] and the papers cited therein. Generally, the impedance can be estimated either in the frequency domain, e.g., using the discrete Fourier transform (DFT) [1–4] or band-pass filters [5], or in the time domain applying model-based methods [6–10]. The estimation result is typically the frequency response $Z_g(j\omega)$ or the values of $L_g$ and $R_g$. The impedance estimate can be used, e.g., to detect islanding conditions [1,5] or to optimize converter control tuning [6].

When the per-phase impedances are unequal, the grid impedance is unbalanced (asymmetric). Then, the space-vector model of the impedance becomes an asymmetric $2 \times 2$ transfer-function matrix in stationary $\alpha\beta$ coordinates [11]. Real-time estimation of the unbalanced impedance has been addressed in [12–14]. Compared to estimation of the balanced impedance, estimation of the unbalanced impedance is more complicated due to increased amount of parameters. In [12] and [13], the estimation relies on the assumption of the inductive-resistive grid. In [12], a model-based recursive least squares algorithm is used. In [13], the estimation is based on dual-frequency injection and DFTs of the currents and voltages. If the assumption of the inductive-resistive grid is violated, such as in the case of capacitive elements in the grid, these methods provide biased results. In a wavelet-based estimation method [14], this assumption is not needed. However, the method requires measurement of three line-to-neutral voltages. Therefore, contrary to the methods in [12, 13], it cannot be applied if the neutral-point is not available or only line-to-line voltages are measured.

Estimation of a $2 \times 2$ impedance matrix in synchronous (dq) coordinates has been studied in [3, 15] assuming time-invariant impedance during the estimation. If an $\alpha\beta$-asymmetric impedance is transformed to synchronous coordinates, the resulting $2 \times 2$ transfer-function matrix becomes time variant,
The model can be transformed to synchronous coordinates resulting in

\[
\mathbf{u}_g^s(s) = Z_g^s(s)\mathbf{i}_g^s(s) + \mathbf{e}_g^s(s)
\]

where \( \mathbf{i}_g^s = [i_{ga}, i_{gb}, i_{gc}]^T \) is the grid current vector and \( Z_g^s \) is the grid impedance

\[
Z_g^s(s) = \begin{bmatrix} Z_{\alpha\alpha}(s) & Z_{\alpha\beta}(s) \\ Z_{\beta\alpha}(s) & Z_{\beta\beta}(s) \end{bmatrix}
\]

The model can be transformed to synchronous coordinates resulting in \( \mathbf{u}_g(s) = Z_g(s)\mathbf{i}_g(s) + \mathbf{e}_g(s) \), where

\[
\mathbf{u}_g = \exp\{-\mathbf{J}\varphi_g(t)\} \mathbf{u}_g^s, \quad \mathbf{e}_g = \exp\{-\mathbf{J}\varphi_g(t)\} \mathbf{e}_g^s, \quad \mathbf{i}_g = \exp\{-\mathbf{J}\varphi_g(t)\} \mathbf{i}_g^s,
\]

and \( Z_g \) is the grid impedance with the elements \( Z_{\alpha\alpha}, Z_{\alpha\beta}, Z_{\beta\alpha}, \) and \( Z_{\beta\beta} \). In the transformation, \( \varphi_g \) is the angle of the coordinate system, and \( \mathbf{J} = \begin{bmatrix} 0 & -1 \\ 1 & 0 \end{bmatrix} \).

The impedance \( Z_g^s(s) \) can represent a balanced or unbalanced system. To provide an example, let us consider a three-phase inductive-resistive grid with the per-phase inductances \( L_{ga}, L_{gb}, \) and \( L_{gc} \), and resistances \( R_{ga}, R_{gb}, \) and \( R_{gc} \). The per-phase impedances are \( Z_a(s) = sL_{ga} + R_{ga}, \) \( Z_b(s) = sL_{gb} + R_{gb}, \) and \( Z_c(s) = sL_{gc} + R_{gc} \). If the coupling between the phases is neglected, the phase voltages at the PCC can be written

\[
[u_{ga}, u_{gb}, u_{gc}]^T = Z_g^{abc}[i_{ga}, i_{gb}, i_{gc}]^T + [e_{ga}, e_{gb}, e_{gc}]^T
\]
where $Z_{abc}^g$ is a diagonal matrix $Z_{abc}^g = \text{diag}(Z_a, Z_b, Z_c)$. Translating the phase quantities into space vectors as presented in [11], the impedance of this system in stationary coordinates becomes

$$Z_{abc}^s(s) = T_{32} Z_{abc}^g(s) T_{23} = \begin{bmatrix} \frac{4Z_a(s)+Z_b(s)+Z_c(s)}{6} & \frac{\sqrt{3}Z_c(s)-Z_b(s)}{6} & \frac{Z_b(s)+Z_c(s)}{6} \\ \frac{\sqrt{3}Z_c(s)-Z_b(s)}{6} & 0 & \frac{Z_b(s)+Z_c(s)}{6} \\ \frac{Z_b(s)+Z_c(s)}{6} & \frac{Z_b(s)+Z_c(s)}{6} & 0 \end{bmatrix}, \quad T_{32} = \begin{bmatrix} \frac{2}{3} & -\frac{1}{3} & -\frac{1}{3} \\ 0 & \frac{1}{\sqrt{3}} & -\frac{1}{\sqrt{3}} \\ \frac{1}{\sqrt{3}} & \frac{1}{\sqrt{3}} & \frac{1}{\sqrt{3}} \end{bmatrix}$$

(4)

where $T_{23} = (3/2) T_{32}^T$. From (4), it can be seen that if the per-phase impedances are equal $Z_a = Z_b = Z_c = Z_g$, the impedance $Z_{abc}^s$ reduces to the diagonal matrix $Z_{abc}^s = \text{diag}(Z_g, Z_g)$, but in the case of an unbalanced impedance, all elements of $Z_{abc}^s$ are generally nonzero.

3. Impedance Estimator

The frequency response of the grid impedance is estimated at a single frequency $\omega_e$ or multiple frequencies $\omega_{e,1}, \ldots, \omega_{e,n}$ at a time. For the estimation, a pulsating excitation signal $u_{c,e}^s$ is added to the converter voltage reference $u_{c,e,\text{ref}}^s$ as shown in Fig. 1.

3.1. Single-Frequency Estimation

For the single-frequency estimation, the excitation signal is

$$v_{c,e}^s(t) = \begin{bmatrix} v_{e,\alpha}^s(t) \\ v_{e,\beta}^s(t) \end{bmatrix} = e^{j\vartheta_e(t)} \begin{bmatrix} v_e \sin(\omega_e t) \\ 0 \end{bmatrix}, \quad J = \begin{bmatrix} 0 & -1 \\ 1 & 0 \end{bmatrix}$$

(5)

where $v_e$ is the magnitude and $\vartheta_e$ determines the angle of the pulsating vector in stationary coordinates. In this work, the angle is alternated between $\vartheta_e = 0$ and $\vartheta_e = \pi/2$ to excite the system in $\alpha$-axis and $\beta$-axis directions periodically

$$\vartheta_e(t) = 0, \quad \text{when} \quad \sin(\pi t/T_1) > 0$$

$$\vartheta_e(t) = \pi/2, \quad \text{otherwise}$$

(6)

where $T_1$ is the sampling interval of the impedance estimation. Two different injection directions are needed to provide two linearly independent tests for the impedance matrix identification. It is assumed that the injection-frequency component is not present in the grid voltage or current spectrum before the injection. Therefore, the frequency $\omega_e$ of the signal is a grid-frequency interharmonic. An excitation signal corresponding to (5) and (6) is shown in Fig. 2.

Fig. 3(a) shows the structure of the proposed impedance estimator in the case of single-frequency estimation. The impedance estimator utilizes the PCC voltage and current samples measured by the converter. Sampled line-to-line voltages and phase currents are turned into corresponding space-vector components $u_{g,\alpha}$, $u_{g,\beta}$, $i_{g,\alpha}$, and $i_{g,\beta}$. The complex phasors $I_{g,\alpha}(\omega_e)$ and $I_{g,\beta}(\omega_e)$ of the current components and the complex phasors $U_{g,\alpha}(\omega_e)$ and $U_{g,\beta}(\omega_e)$ of the voltage components are continuously calculated in real-time applying the modulated SDFT algorithm [18]. The internal structure of the SDFT module used in the proposed method is shown in Fig. 3(b). It is to be noted that due to the sliding window of the SDFT
Since the injection-frequency component of the algorithm, the calculated phasors are rotating as shown in Fig. 2.

Since the injection-frequency component of $e_i^g$ is assumed to be zero, the phasors of the PCC voltage at $\omega_e$ can be expressed as

$$
\begin{bmatrix}
U_{g\alpha 1}(\omega_e) & U_{g\alpha 2}(\omega_e) \\
U_{g\beta 1}(\omega_e) & U_{g\beta 2}(\omega_e)
\end{bmatrix} = Z_g^e(j\omega_e) \begin{bmatrix} I_{g\alpha 1}(\omega_e) & I_{g\alpha 2}(\omega_e) \\
I_{g\beta 1}(\omega_e) & I_{g\beta 2}(\omega_e)\end{bmatrix}
$$

(7)

where $U_{g\alpha 1}, U_{g\beta 1}, I_{g\alpha 1}, I_{g\beta 1}$ are obtained during the first injection direction (first test) and $U_{g\alpha 2}, U_{g\beta 2}, I_{g\alpha 2}, I_{g\beta 2}$ during the second injection direction (second test). Two different tests provide enough information for the impedance matrix estimation, and the estimate is directly calculated as

$$
\hat{Z}_g^e(j\omega_e) = \hat{R}_g^e(\omega_e) + j\hat{X}_g^e(\omega_e) = U_{m}(\omega_e)I_m^{-1}(\omega_e)
$$

(8)

where $\hat{R}_g^e$ and $\hat{X}_g^e$ are the resistive and reactive parts of the impedance matrix.

The phasors of the voltages and currents are applicable to impedance estimation only when the system is in steady state. Since the angle $\vartheta_e$ of the pulsating injection (6) is periodically changed, the steady state has to be reached before the next change in $\vartheta_e$. Therefore, the impedance estimation is synchronized with the excitation signal such that the rotating phasors of the currents and voltages for (8) are sampled just before the change in $\vartheta_e$ with the sampling frequency of $f_s = 1/T_1$ as illustrated in Fig. 2. The steady-state requirement sets the minimum value for the impedance estimation interval $T_1$. Moreover, the time required to reach the steady state depends on the settling time of the existing control system and phasor calculation. The settling time of the phasor calculation is related to the buffer length $N$ of the SDFT modules, and $N$ depends on the desired frequency resolution and sampling frequency $f_s$ of the DFT. Therefore, the settling time of the phasor calculation is approximately one fundamental period of the DFT frequency resolution. The frequency resolution has to be a common divisor of the fundamental frequency of the grid and the injection frequency. To give an example, if the frequency resolution is 10.
Hz and the sampling frequency is $f_s = 10$ kHz, the buffer size is $N = 1000$ and the settling time of the SDFT modules and the phasor calculation is approximately $1/(10 \text{ Hz}) = 100$ ms.

When the system is in steady state, the calculated phasors can be averaged to reduce effect of measurement noise and to increase accuracy of the impedance estimation. For averaging the phasors, the SDFT structure presented in [18] is augmented with a low-pass filter (LPF). In this paper, a first-order LPF with the transfer function of $\alpha f/(s + \alpha f)$ and bandwidth of $\alpha f$ is applied due to its simplicity. It is transformed to discrete time via the mapping $s \rightarrow (z - 1)/T_s$. Alternatively, the LPF can be, e.g., a moving-average filter which is naturally averaging the phasors over its buffer length. The LPF is inserted inside the resonator, as shown in Fig. 3(b), where the signals are dc-valued in steady state.

In the SDFT module, the resonator coefficient $c_e = \exp(-j\omega_1 T_s)$ selects the frequency of the single DFT bin for the phasor calculation as explained in [19]. It is to be noted that, $\omega_e$ has to be integer multiple of the angular frequency resolution of the SDFT module as in the case of the conventional DFT. Finally, as Fig. 3(b) shows, only a few multiplications and additions are needed in one sampling period $T_s = 1/f_s$ per SDFT module meaning low computation burden that is an advantage in a real-time control system.

### 3.2. Multi-Frequency Estimation

The presented single-frequency estimator can be easily extended for multi-frequency impedance estimation with the following modifications: 1) $n$ pulsating sinusoidal components are added at the frequencies of $\omega_{e,1}, \ldots, \omega_{e,n}$ to the converter voltage reference. 2) The SDFT modules are extended for multi-frequency phasor calculation adding extra resonators, one per frequency, in parallel with the first resonator as demonstrated in Fig. 4. 3) At every frequency component $\omega_{e,1}, \ldots, \omega_{e,n}$, the calculated phasors are sampled and impedance estimate (8) is calculated as explained in the previous section for a single frequency component.

The multi-frequency excitation signal is

$$v_e(t) = \sum_{k=1}^{n} c_{e,k} \sin(\omega_{e,k} t + \phi_{e,k})$$

(9)

where $v_{e,k}$ is the magnitude and $\phi_{e,k}$ is the angle of the $k$:th pulsating vector. In addition, every pulsating component can have different initial phase $\phi_{e,k}$ at $t = 0$. The angles $\phi_{e,k}, k = 1 \ldots n$, are alternated periodically in order to excite the system in different directions and to provide linearly independent tests for the impedance matrix identification. If the angles $\phi_{e,k}$ are set as in (6) for all components, the components will excite the system in the same direction, i.e., in $\alpha$-axis and $\beta$-axis directions periodically. On the contrary, if the angle $\phi_{e,k}$ is not the same for every component, the magnitude $|v_e(t)|$ of the signal

![Fig. 4: SDFT module for multi-frequency phasor calculation. The resonator coefficients are set corresponding the frequencies of interest, i.e., $c_{e,1} = \exp(-j\omega_{e,1} T_s)$, ..., $c_{e,n} = \exp(-j\omega_{e,n} T_s)$.

$$v_e(t) = \sum_{k=1}^{n} c_{e,k} \sin(\omega_{e,k} t + \phi_{e,k})$$

(9)
can be reduced [20]. For example, in the case of dual-frequency excitation, selecting the angles to provide orthogonal components would minimize the magnitude. The initial phases $\phi_{e,k}, k = 1 \ldots n$, provide an additional degree of freedom to shape $v_{e}^{k}(t)$. For multi-tone signals, several approaches to have been presented to select these initial phases to minimize the signal magnitude or the crest factor, e.g., [20,21].

### 3.3. Estimation in Synchronous Coordinates

In Sections 3.1 and 3.2, the proposed estimator is introduced in stationary coordinates for $\alpha\beta$-asymmetric systems. In order to estimate dq-asymmetric impedances, the estimator can be transformed to synchronous (dq) coordinates. Only two modifications are needed. Firstly, the excitation signal $v_{e}$ is generated in synchronous coordinates similarly to (5) or (9) and transformed to stationary coordinates as $v_{e}^{k} = \exp \{J\vartheta_{g}(t)\} v_{e}$, where $\vartheta_{g}$ is the angle of the synchronous coordinate system. Secondly, the PCC voltage and current vectors are transformed to synchronous coordinates before the SDFT-based phasor calculation and impedance estimation as $u_{g} = \exp \{-J\vartheta_{g}(t)\} u_{g}^{s}$ and $i_{g} = \exp \{-J\vartheta_{g}(t)\} i_{g}^{s}$, respectively. The angle $\vartheta_{g}$ for the transformations can be obtained, e.g., from a phase-locked loop (PLL) tracking the PCC voltage. The PLL has to include a band-stop filter at the injection frequency to provide injection-frequency free angle. If $\vartheta_{g}$ oscillates at the injection frequency, the estimated impedance can be significantly biased. With these modifications, the proposed estimator operates in synchronous coordinates and produces an impedance estimate in these coordinates instead of stationary coordinates. The estimation of a dq-asymmetric impedance may be of interest if other converters connected to the same grid have a notable impact on the impedance seen from the PCC.

### 3.4. Comparison

Properties of the proposed estimator are compared with those of the state-of-the-art converter-based estimators capable for unbalanced grid-impedance estimation. Whereas the methods in [13, 14] estimate the impedance in the natural (abc) reference frame, the method in [12] in the $\alpha\beta$ reference frame, and the methods in [3, 15] in the dq reference frame, the proposed estimator can be configured to estimate either an $\alpha\beta$- or dq-asymmetric impedance. Access to the neutral-point potential and phase-to-neutral voltage measurements are required in [14]. On the contrary, when the space vectors are applied in the estimation, line-to-line voltage measurements can be used and neutral point potential is not needed. The estimation methods in [12, 13] assume inductive-resistive grid model whereas the impedance model is not fixed in [3, 14] and in the proposed method. In [15], a parametric differential-equation model is iteratively fitted to measured data. Although a parametric grid model is obtained as an estimation result in [15], the model order and structure has to be determined which complicates the estimation.

The DFT is used in the frequency-domain impedance estimation in [3, 13] which requires collecting the measurement data in a buffer before the impedance estimate can be calculated. If the number of data points is large, the calculation has to be run as a background process of the converter delaying the estimation. The method in [15] also requires buffers and background processing the measured data due to its iterative nature. On the contrary, the impedance estimate is updated recursively on a sample-by-sample basis in [12]. The advantage of the recursive calculation is that the computational load is spread over the excitation period. In the proposed method, data buffers are needed for the SDFT but the phasors are recursively calculated on a sample-by-sample basis. This reduces the computational burden compared to the conventional DFT in the case of a few frequency components in the estimation.

As presented in Sections 3.1 and 3.2, the proposed estimator can be configured for selective single- or multi-frequency estimation in a flexible manner thanks to its modular structure. In [13] two frequency components are required in the excitation signal to obtain the estimated inductance and resistance values. In [3, 12, 15] the excitation signal has wide frequency band since it is either required in the selected parametric estimation method [12, 15] or a wide-band frequency response is of interest [3]. Even though in the proposed method, the number of frequency components can be increased in the excitation signal and SDFT modules, the approach [3] with the pseudo-random binary signal excitation and the conventional DFT becomes more attractive if tens or hundreds of frequency components are of interest.
4. Results

4.1. Simulations

The proposed estimator is first verified with simulations considering a 400-V 12.5-kVA grid converter system. The existing control system [22] comprises a current-control loop and a PLL. The switching frequency of the converter is 5 kHz. The sampling frequency of the control system and the SDFT is $f_s = 10$ kHz. The buffer size for the SDFT is $N = 1000$ samples per measured current or voltage component. The impedance estimation interval is set to $T_i = 200$ ms to ensure that steady state is reached for the phasor calculation. A single-frequency excitation signal, given in (5) and (6), is used. Its frequency is an inter-harmonic of $\omega_e = 2\pi \cdot 110$ rad/s, and its magnitude is $v_e = 0.02$ p.u. During the verification test, the converter injects the power of $p_g = 0.5$ p.u. to the 50-Hz grid.

Fig. 5(a) shows the estimated elements of the grid impedance matrix when the proposed method is started at $t = 1$ s to estimate an unbalanced inductive-resistive grid impedance. Per-phase inductances are $L_{ga} = L_{gc} = 5.5$ mH and $L_{gb} = 8.5$ mH and resistances are $R_{ga} = R_{gc} = 0.5$ Ω and $R_{gb} = 1.9$ Ω. At $t = 3$ s the b-phase inductance $L_{gb}$ is reduced from 8.5 mH to 5.5 mH and resistance $R_{gb}$ is reduced from 1.9 Ω to 0.5 Ω, i.e., the impedance becomes balanced. If the inductive-resistive grid is assumed in the estimator, the estimated impedance matrix elements can be translated to per-phase inductance and resistance estimates based on (4) as $Z_a = (3Z_{\alpha\alpha} - Z_{\beta\beta})/2$, $Z_b = Z_{\beta\beta} - \sqrt{3}/2 \cdot (Z_{\alpha\beta} + Z_{\beta\alpha})$, and $Z_c = Z_{\beta\beta} + \sqrt{3}/2 \cdot (Z_{\alpha\beta} + Z_{\beta\alpha})$. The per-phase estimates are demonstrated in Fig. 5(b). As the figure shows, the method correctly estimates the unbalanced and balanced grid impedance.

The capability to estimate a dq-asymmetric impedance is also verified with simulations. The estimator is configured as in the first simulation test and transformed to synchronous coordinates as described in Section 3.3. Fig. 6 shows the estimated elements of the impedance matrix when the estimator is started at $t = 1$ s to estimate a balanced grid impedance $R_g = 1.5$ Ω and $L_g = 8.5$ mH. At $t = 3$ s, another 12.5-kVA three-phase converter with an LCL filter ($L_1 = 3.3$ mH, $C = 8.8$ µF, $L_2 = 3.0$ mH) is connected to the PCC and it starts to inject active power of $p_g = 0.5$ p.u. to the grid. Its switching frequency is 4 kHz, and its control system comprises proportional-integral (PI) grid current controller in dq coordinates.
12.5-kV A converter under test

Grid connection

Adjustable impedance

dSPACE system

1.4 Ω 3 mH

5.5-mH chokes

Fig. 7: (a) Experimental test setup. The converter control software and the impedance estimator are running on the dSPACE DS1006 processor board where the sampling of the measured signals is synchronized with the pulse-width modulation (PWM). (b) Adjusted impedance under test.

Fig. 8: Measurement results: (a) excitation voltage; (b) spectra of the PCC voltage and current during the estimation.

As Fig. 6 shows, the other converter at the PCC significantly changes the overall grid impedance seen by the estimating converter (dashed-lines show the theoretical overall impedance). Nevertheless, the proposed estimator accurately estimates the impedance in dq coordinates with and without the other converter in the system. Furthermore, it was observed that the switching frequency components originating from the other converter can cause significant bias in the estimates. To alleviate this bias, a simple 3-kHz first-order filter was employed as an anti-aliasing filter for the PCC voltage and current measurements.

4.2. Experiments

The verification test was experimentally repeated. A diagram of the setup used in the experiments is shown in Fig. 7(a). The unbalanced impedance is emulated with 5.5-mH chokes in all three phases and an extra 3-mH choke and resistance of 1.4 Ω in the b phase as demonstrated in Fig. 7(b). The chokes are connected between the PCC and the 50-Hz electric power system. Fig. 8(a) shows the excitation signal during the experiment, and Fig. 8(b) shows the PCC current and voltage spectra when the excitation signal is enabled. The spectra are calculated over five grid-voltage periods (0.1 s) during one injection direction when the grid impedance is unbalanced. It can be seen that the magnitudes of the injection-frequency components are at the same level as typical grid current harmonics. The −50 Hz negative sequence component originates from the unbalanced impedance.

Fig. 9(a) shows the estimated elements of the grid impedance matrix and Fig. 9(b) the estimated per-phase inductances and resistances. The estimated per-phase impedances are obtained from the elements of the impedance matrix as in Section 4.1. The proposed method is started at \( t = 1 \) s, and the extra 3-mH choke and resistance in the b-phase is bypassed around \( t = 3.5 \) s [cf. Fig 7(b)] to change the impedance from unbalanced to balanced. As the results demonstrate, the proposed method can identify the balanced and unbalanced grid impedance and detect changes well in real time. Compared to the simulation results, the estimated resistive components are influenced by the noise in the measured signals causing some variance. The effect of noise can be reduced with averaging the calculated phasors as explained in Section 3.1. Here, the LPF bandwidth for this purpose was set to \( \alpha_f = 2\pi \cdot 10 \) Hz.

Finally, the same test was repeated using a multi-frequency excitation signal (9) to demonstrate the
Fig. 9: Measurement results with single-frequency excitation: (a) estimated elements of the grid impedance matrix $\hat{Z}_g^s(\omega_e) = \hat{R}_g^s(\omega_e) + j\hat{X}_g^s(\omega_e)$; (b) estimated per-phase inductances and resistances.

Fig. 10: Measurement results with multi-frequency excitation: (a) estimated a-phase inductances and resistances; (b) estimated b-phase inductances and resistances; (c) estimated c-phase inductances and resistances.

multi-frequency estimation. The injection frequencies are $\omega_{e,1} = 2\pi \cdot 110$ Hz, $\omega_{e,2} = 2\pi \cdot 120$ Hz, and $\omega_{e,3} = 2\pi \cdot 130$ Hz, and the amplitudes of the signals are $v_{e,1} = v_{e,2} = v_{e,3} = 0.02$ p.u. For simplicity, the injection angles $\vartheta_{e,k}$ are selected according to (6) and the phase shifts are $\phi_{e,k} = 0$ for all $k = 1 \ldots 3$. Fig. 10 shows the estimated per-phase inductances and resistances at all injection frequencies. Again, the impedance change is well detected and the estimation results at these frequencies agree with each other. A drawback of the multi-frequency estimation is increased distortion of grid currents due to multiple injection components. However, simultaneous estimation of multiple frequency components can provide more information of the grid impedance within same estimation interval. In addition, calculating the average of the estimated resistances obtained at different frequencies, e.g., $[R_a(110 \text{ Hz}) + R_a(120 \text{ Hz}) + R_a(130 \text{ Hz})]/3$, helps to reduce variance of the estimated resistances.

5. Conclusions

This paper presented a real-time grid impedance estimation method for three-phase power converters. While a periodic excitation signal is added to the converter voltage reference, the proposed method can continuously estimate either unbalanced or balanced impedance at the injection frequency. Due to lightweight computation of the SDFT algorithm used in the estimation, the proposed method can be easily implemented in a converter control system. The estimated impedance can be internally applied in the control system, e.g., to improve control performance. In remote monitoring of converters, the estimated impedance may provide added value when analyzing a converter or power system status or possible grid–converter interactions. In addition to grid converter systems, the presented estimation technique can be applied in other converter systems with similar interfaces, such as in motor drives.
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